

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,940,875 B2
APPLICATION NO. : 10/643792
DATED : September 6, 2005
INVENTOR(S) : Mesecher et al.

Page 1 of 8

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Delete the title page and substitute therefor the attached title page.

ON THE TITLE PAGE

At Item (57), line 1, before the word “for”, delete “reciever” and insert therefor
--receiver--.

IN THE DRAWINGS

Please replace Figures 2-5 and 11 with replacement Figures 2-5 and 11 attached
hereto.

IN THE SPECIFICATION

At column 2, line 13, after the word “a”, delete “B-CDMATM” and insert
therefor --B-CDMATM--.

At column 2, line 40, after the word “present”, delete “inventor” and insert
therefor --invention--.

At column 5, line 53, after the word “mixer”, delete “122” and insert therefor
--110--.

At column 5, line 67, equation 2, delete “ $m = \sqrt{(I^{2+Q^2})}$ ” and insert therefor
-- $m = \sqrt{I^2 + Q^2}$ --.

At column 9, line 55, after the word “The”, delete “anaysis” and insert therefor
--analysis--.

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Page 2 of 8

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

IN THE CLAIMS

In claim 24, at column 12, line 42, after the word "response", insert --to--.

This certificate supersedes the Certificate of Correction issued June 10, 2008.

Signed and Sealed this

Fifteenth Day of July, 2008

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS
Director of the United States Patent and Trademark Office

(12) **United States Patent**
Mesecher et al.

(10) Patent No.: **US 6,940,875 B2**
(45) Date of Patent: ***Sep. 6, 2005**

(54) **CONTINUOUSLY ADJUSTED-BANDWIDTH
DISCRETE-TIME PHASE-LOCKED LOOP**

(75) Inventors: David K. Mesecher, Huntington
Station, NY (US); Rui Yang,
Greenlawn, NY (US); Ramon Cerda,
West Babylon, NY (US)

(73) Assignee: **InterDigital Technology Corporation,**
Wilmington, DE (US)

(*) Notice: Subject to any disclaimer, the term of this
patent is extended or adjusted under 35
U.S.C. 154(b) by 0 days.

This patent is subject to a terminal dis-
claimer.

(21) Appl. No.: **10/643,792**

(22) Filed: **Aug. 19, 2003**

(65) **Prior Publication Data**

US 2004/0032842 A1 Feb. 19, 2004

Related U.S. Application Data

(63) Continuation of application No. 09/558,686, filed on Apr.
24, 2000, now Pat. No. 6,608,826, which is a continuation
of application No. 08/871,109, filed on Jun. 9, 1997, now
Pat. No. 6,055,231.

(60) Provisional application No. 60/037,914, filed on Mar. 12,
1997.

(51) Int. Cl.⁷ **H04J 3/06**

(52) U.S. Cl. **370/516; 370/252; 370/342;
375/136; 375/294**

(58) Field of Search **370/252, 328,
370/329, 335, 342, 441, 463, 516, 330,
320; 375/136, 147, 294, 344, 206, 200,
150, 222; 379/406.08**

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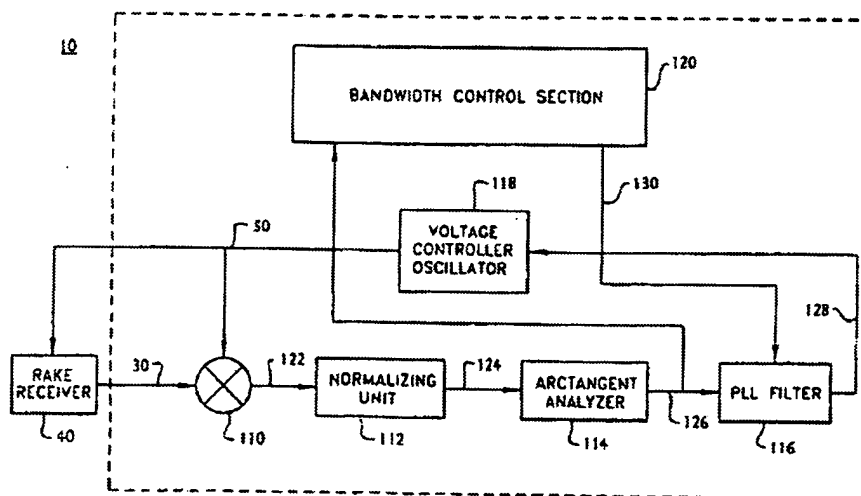
Primary Examiner—John Pezzlo

(74) Attorney, Agent, or Firm—Volpe and Koenig, P.C.

(57) **ABSTRACT**

A receiver for receiving a CDMA communication signal that is wirelessly transmitted includes a system for correcting phase errors in an information signal which has been transmitted. The correction system comprises circuitry for generating a mixing signal and for combining the mixing signal with said information signal to produce a correction signal. An analyzer analyzes the phase of said correction signal and generates an error signal based on the deviation of the analyzed phase from a reference phase. A bandwidth controller which recursively adjusts the phase of the correction signal such that the phase of said correction signal is substantially equal to said reference phase. The bandwidth controller then selects a bandwidth within an adjustable range based on the correction signal, estimates an offset by interrogating the error signal, and modifies said correction signal by the offset.

25 Claims, 14 Drawing Sheets



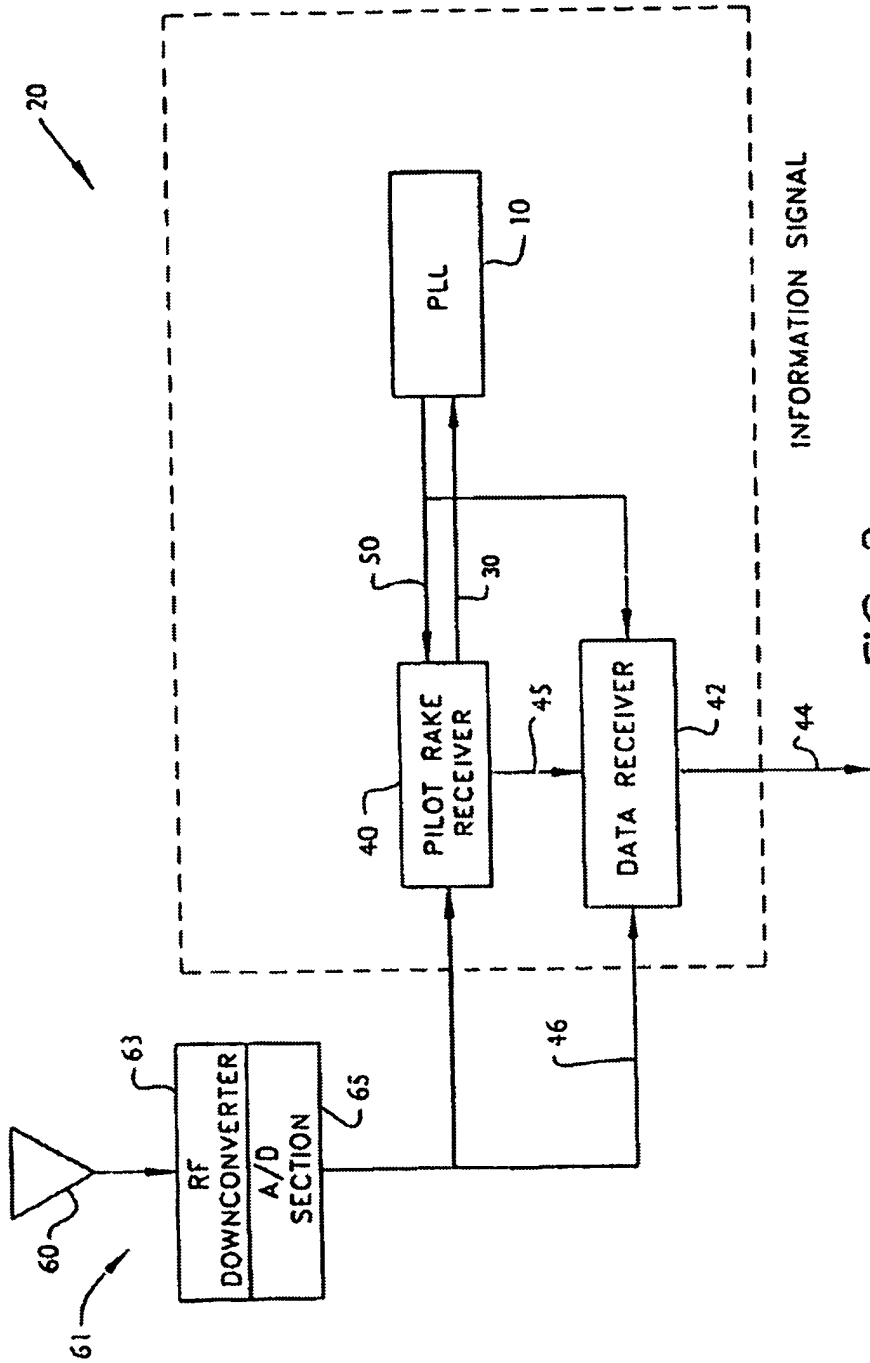
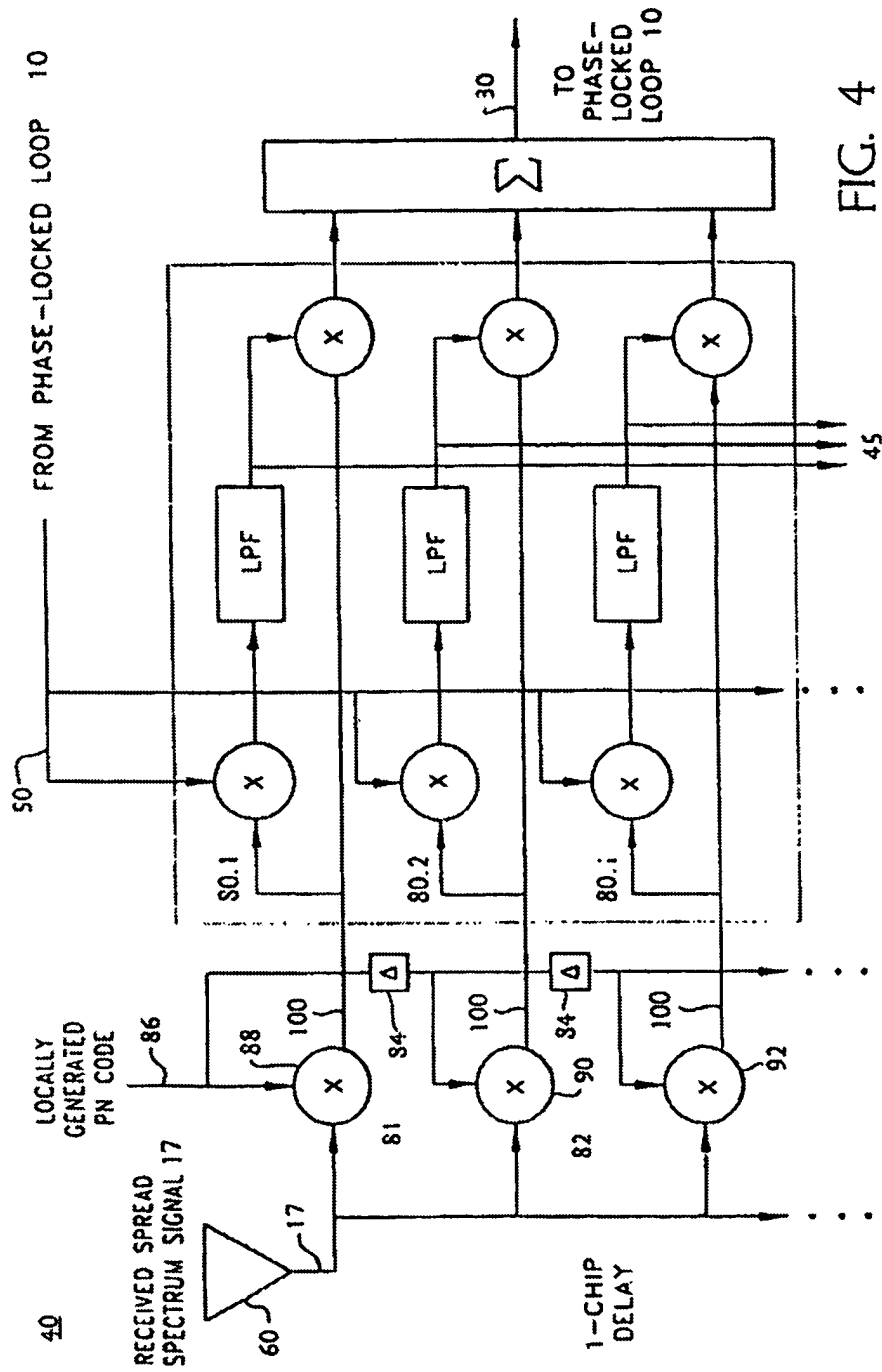


FIG. 3



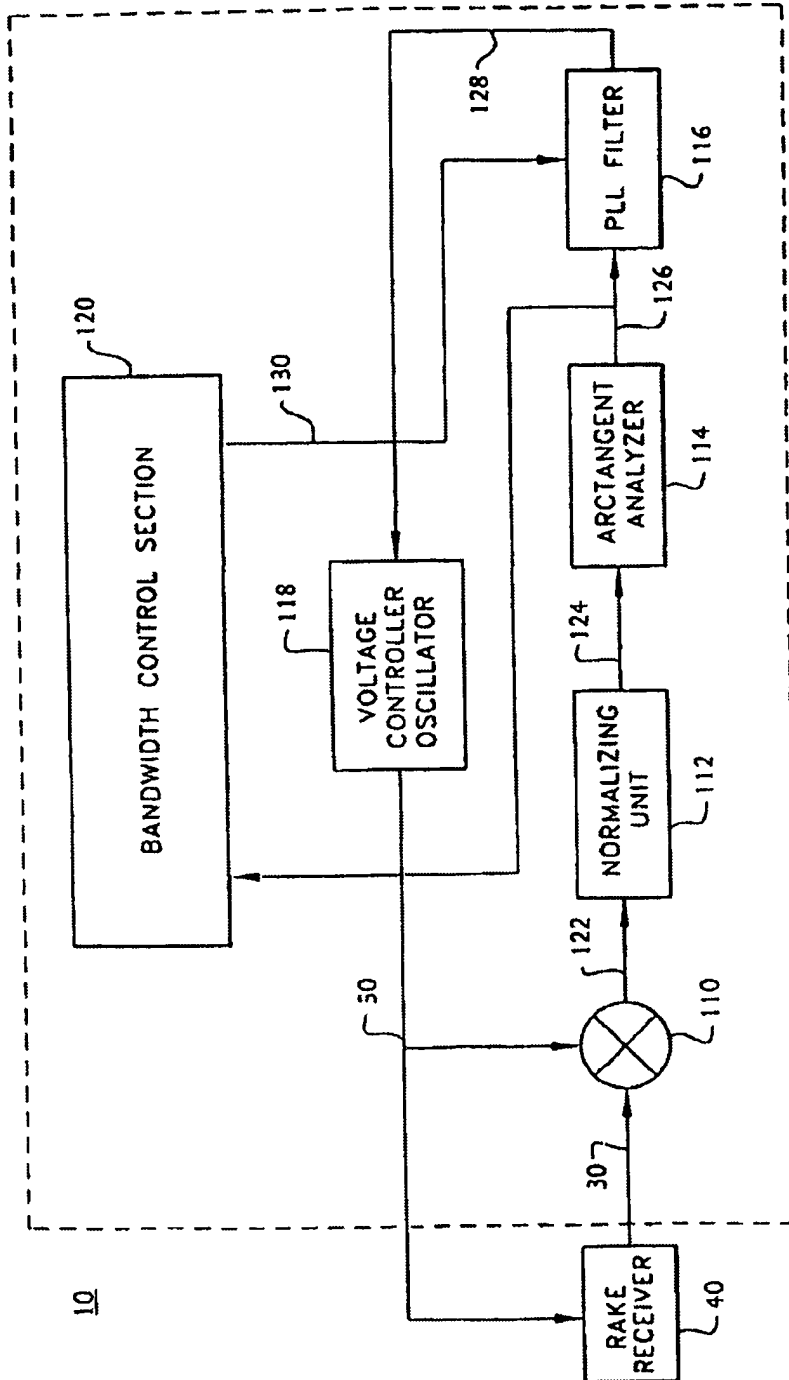


FIG. 5

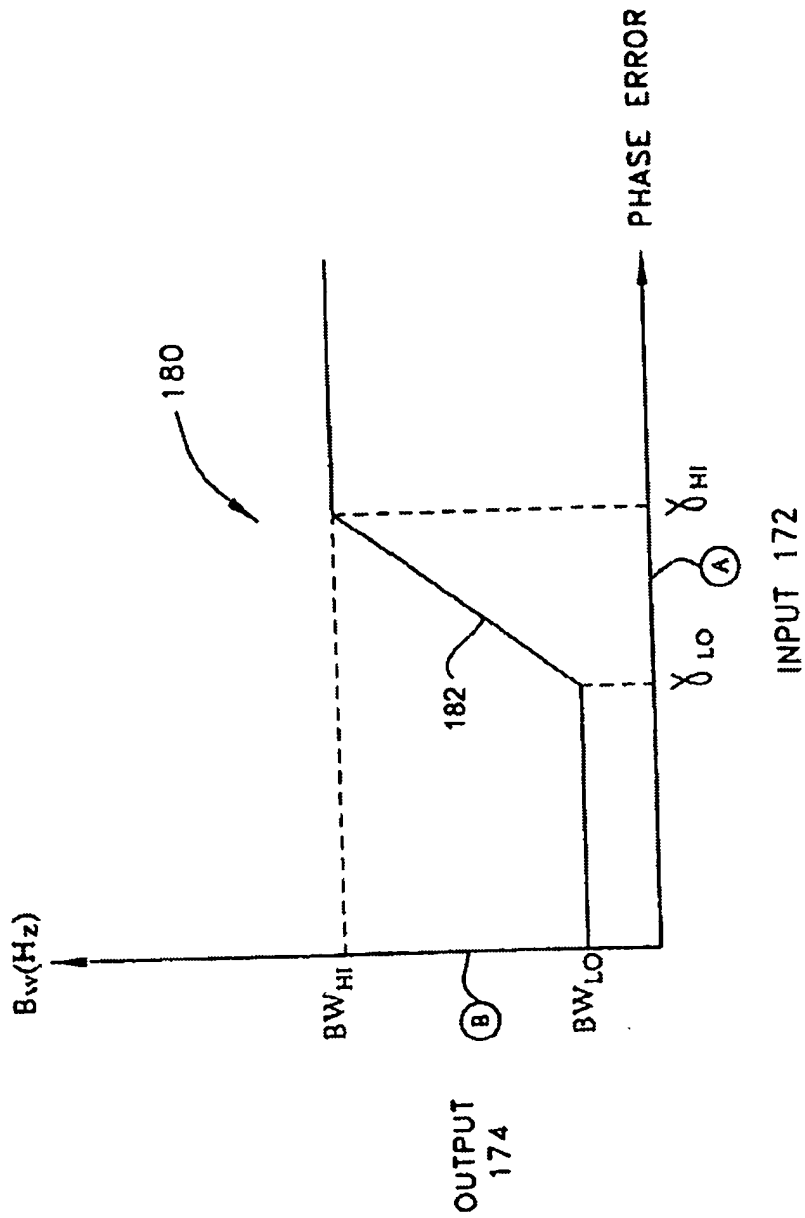


FIG. 11